

Meeting No: 7

Date: 18/05/2018

Head of the department has Convened the meeting of BOS in the board room at 10:00 A.M on 18/05/2018 to Discuss

Agenda for meeting

- (1) To welcome the BOS members
- (2) To review the Ad-hoc BOS meeting
- (3) To discuss, review and approve the proposed Curriculum of B.Tech In EEE for the autonomous system commencing from the Academic Year 2018- also finalize the Syllabus for Basic Electrical Engineering course and second year syllabus
- (4) To discuss, review and approve the proposed Curriculum of M.Tech In Power Electronics for the autonomous system commencing from the Academic Year 2018-2019.
- (5) Any other matter with the Permission of the chair

minutes of meeting

Agenda-1: To welcome the BOS members.

Resolution: 1. The chairman of BOS has invited the members of BOS to All the faculty members and Faculty of EEE to the meeting of BOS.

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Agenda - I : To review the Adhoc BOS meeting held on 1st may 2018.

Resolution: members of the BOS are reviewed the meeting held on 1st may 2018. as follows.

1. members are reviewed the course structure for UG and PG programs in the Autonomous structure commencing from the academic year 2018-19.
2. members ~~are~~ reviewed the credits and lectures for each course of UG and PG programs.

Agenda - II : To discuss review and approve the proposed curriculum of B.Tech in EEE for the autonomous system commencing from the academic year 2018-19 also finalize the syllabus for Basic Electrical Engineering course.

Resolution: The members are reviewed and suggested following points in the proposed curriculum of B.Tech in EEE

1. In Basic Electrical Engineering course members suggested to specify the topics in a narrow way. They also modify some of the topics in the Basic Electrical Engineering syllabus.
2. In Basic Electrical Engineering laboratory members suggested to modify the experiments in a easy and simple manner
3. In Control Systems course members suggested to delete 4 & 5 units and modify them as lead, lag & lead lag compensator design

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for time domain & frequency domain case

4. In electrical circuit analysis course members suggested to specify the topics with simple problems.
5. members suggested to add a self learning topic in each unit and mention it in italic also suggested that the Assessment of this topic should be done internally.
6. members suggested to modify the elective courses such that there must be a continuous course or follow the electives mentioned in AICTE model curriculum
7. members suggested to split the total no. of hrs into 45 lectures and 15 tutorials in all courses also make uniform hrs distribution for each unit
8. members suggested to do mapping of course outcomes with program outcomes with why and how to map
9. members suggested to mention Power System protection and ~~optimization~~ techniques as compulsory courses rather than electives.
10. members suggested to modify the course title from Biology for Engineers to Biology as mentioned in the AICTE. Further they suggested to eliminate this course if possible
11. Since it was not related to the Electrical Engineering
11. members suggested to ensure the credits for each course and total ^{marks} credits must be uniform for all the departments.

OK ✓

~~Done~~

Agenda iv: To discuss review and approve the Proposed Curriculum of m.Tech in Power Electronics for the autonomous Systems commencing from the academic year 2018-19.

Resolution: members reviewed the proposed curriculum of m.Tech in power electronics for the autonomous Systems commencing from the academic year 2018-19 as follows.

1. members suggested that there should be six theory and one laboratory for each semester in the first year.
2. members also suggested to include seminar in the 1st semester and mini project in the second semester of first year.
3. members suggested to clearly define the term paper and the evaluation process.
4. members suggested to change total no. of marks to 1900 along the lines of other depts.
5. course objectives for some of the courses were not included in the syllabus for example optimization Techniques etc should be included as per the suggestions of the members.
6. members has suggested to include the co-PO mapping for all the courses.

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7. members has suggested to remove power quality laboratory from the 1st semester and Digital signal processing laboratory from the second semester.
8. members has suggested to change the optimal & adaptive control syllabus in the unit -5 MIT rule and self tuning need to be added. Similarly in chapter - linear regulator problem need to be replaced as linear quadratic regulator problem further the topics of cost functions (minimum control, minimum state)

- Agenda :- Any other matter with the permission of chair
- Resolution:
1. members have suggested to conduct a BOS meeting for all the departments on the same day, so that at the end of the day they can discuss common relevant topics like Term paper, Seminar credits and marks allocation.
 2. members have suggested to do proper co-ordination for all the courses with appropriate justification.
 3. Signals & Systems syllabus unit-1 is modified per the suggestions given by the members.

S	S.NO	Name of the member	Signature
	1.	E. Aswani Kumar	E. Aswani Kumar
	2.	Dr. B. Sarvesh	Dr. B. Sarvesh
	3.	Dr. P. Bharani chandra Kumar	Dr. P. Bharani chandra Kumar 18/5/18
	4.	D. Rajesh.	D. Rajesh 18/5/18
	5.	V.L.N. Sasthy	V.L.N. Sasthy 18/5/18
st	6.	P. Ajay Sai Kiran	P. Ajay Sai Kiran
3	7.	K.N.V. Siva	K.N.V. Siva
med	8.	G.V. Appa Rao	G.V. Appa Rao
en	9.	K. Raja Rao	K. Raja Rao
time	10.	M.M. Swamy Naidu	M.M. Swamy Naidu
	11.	Ch. Srinivas	Ch. Srinivas
ais	12.	B.A.S. Kariteek	B.A.S. Kariteek
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